



AF
3600

REPLY UNDER 37 C.F.R § 1.116
EXPEDITED PROCEDURE
EXAMINING GROUP 3629

PATENT
5298-04500/PM00026

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:
Li et al.

Serial No. 09/849,047

Filed: May 4, 2001

For: BURIED LAYER SUBSTRATE
ISOLATION IN INTEGRATED
CIRCUITS

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Group Art Unit: 2822
Examiner: Lewis, M.

Atty. Dkt. No. 5298-04500

8 # / Response
(nt)

I hereby certify that this correspondence is being deposited with the U.S. Postal Service with sufficient postage as First Class Mail in an envelope addressed to: **Box: Non-Fee Amendment**, Commissioner for Patents, Washington, D.C. 20231, on the date indicated below:

September 23, 2003
Date

Pamela Gerik
Pamela Gerik

10/12/03
N/B

RESPONSE AFTER FINAL REJECTION PURSUANT TO 37 CFR § 1.116

Box: AF
Commissioner for Patents
Washington, D.C. 20231

Dear Sir/ Madam:

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This paper is submitted in response to the final Office Action dated July 23, 2003 to further highlight reasons why the application is in condition for allowance.

Remarks begin on page 2 of this paper.